

GLOSSARY OF TECHNICAL TERMS

“3D LUT”	3D look-up table
“8K@60fps”	a video resolution and frame rate specification that represents ultra-high-definition video with a resolution of approximately 8,000 horizontal pixels and a refresh rate of 60 frames per second
“ADAS”	advanced driver assistance systems
“AED”	automated external defibrillator
“AI”	artificial intelligence
“AI-enabled IP”	a cross-domain converged computing architecture optimized for edge applications, utilizing FLEXA synchronous interface technology to integrate independent IP cores such as ISP, VPU and NPU into tightly coupled AI subsystems
“AIGC”	artificial intelligence generated content
“AIoT”	artificial intelligence of things
“AiPaaS”	AI platform as a service
“AR/VR”	augmented reality/virtual reality
“ASIC”	application-specific integrated circuit
“ASIL”	automotive safety integrity levels
“ASIL D”	Automotive Safety Integrity Level D. It is the highest and most stringent risk classification defined in the ISO 26262 standard for functional safety in road vehicles
“BLE”	bluetooth low energy
“BLE 5.3”	bluetooth low energy 5.3
“Chromium OS”	a free, open-source Linux-based operating system designed for web-centric tasks
“CMOS”	complementary metal-oxide-semiconductor
“CPU”	central processing unit
“DDR”	Double Data Rate. It is a type of synchronous dynamic random-access memory (SDRAM) used in computers, servers, smartphones, embedded systems, and other digital electronics
“Dewarp IP”	an IP for distortion correction, licensed to customers for autonomous driving chips

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“EDA tools”	electronic design automation tools
“ESG”	environmental, social, and governance
“ESOP”	employee stock ownership plan
“fabless company”	a company that designs and sells semiconductor chips but outsources the manufacturing to specialized foundries
“FD-SOI”	fully depleted silicon on insulator
“FinFET”	fin field-effect transistor
“FLEXA”	a proprietary synchronous interface technology developed by our Group that integrates independent IP cores — such as ISP, VPU, and NPU — into tightly coupled AI subsystems. By enabling direct hardware-level data flow without DDR memory access, it significantly unloads bandwidth bottlenecks to deliver ultra-low-latency and power-efficient visual computing for edge devices
“FPGA platform”	field-programmable gate array platform
“FreeRTOS”	a real-time operating system kernel for embedded devices
“GHG”	greenhouse gas
“GNSS”	global navigation satellite system
“GPGPU”	general-purpose graphics processing unit
“HB-PoP”	high-bandwidth package-on-package
“HBM”	high bandwidth memory
“HDR”	high dynamic range
“HDR10”	high dynamic range
“HDR10+”	a free, dynamic metadata HDR format that improves upon basic HDR10 by optimizing the picture quality for every scene in a video
“IDM”	integrated device manufacturer
“IoT”	Internet of things

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“IRU”	intelligent rendering unit, a proprietary visual computing technology accelerates graphics rendering and enhance visual output quality in modern computing systems, adopting a distributed rendering architecture
“ISO 26262 standard”	an international standard for the functional safety of electrical and/or electronic (E/E) systems in road vehicles. Its full title is “Road vehicles — Functional safety”
“ISO 27001”	the leading international standard for Information Security Management Systems, providing a risk-based framework to protect data confidentiality, integrity, and availability
“ISO 9001”	a globally recognized standard for quality management which helps organizations of all sizes and sectors to improve their performance, meet customer expectations and demonstrate their commitment to quality
“Linux”	an open-source operating system kernel widely used in embedded systems, servers and consumer devices
“LLM”	large language model
“LPDDR5x”	low power double data rate 5x, a high-bandwidth memory solution used in the High-Performance AIGC Platform
“LPDDR6”	low power double data rate 6, a high-bandwidth memory solution used in the High-Performance AIGC Platform
“MCU”	microcontroller unit
“MIPI”	mobile industry processor interface
“NB-IoT”	narrowband-IoT
“OEM”	original equipment manufacturer
“OSAT”	outsourced semiconductor assembly and test
“PCIe”	peripheral component interconnect express
“PHY”	physical layer interface, referenced in the context of MIPI C/D PHY interfaces
“RTL”	register transfer level
“SAP”	systems, applications, and products
“SDK”	software development kit

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“SerDes”	serializer/deserializer
“SiPaaS”	silicon platform as a service
“SoC”	system-on-chip
“tape-out”	the final step in the design of a chip, where the design data is sent to a foundry for wafer fabrication
“TOPS”	tera operations per second
“TÜV Rheinland”	an independent international testing, inspection and certification organization that certified the company’s ISO 26262 compliant design process
“UCIe”	universal chiplet interconnect express
“VeriHealthi”	a proprietary platform providing integrated health detection solutions spanning from custom silicon design to reference applications