
FUTURE PLANS AND USE OF [REDACTED]

FUTURE PLANS

See “Business — Our Strategies” in this document for a detailed description of our future plans.

USE OF [REDACTED]

We estimate that we will receive net [REDACTED] of HK\$[REDACTED] after deducting [REDACTED] fees and [REDACTED] and estimated [REDACTED] paid and payable by us in the [REDACTED], assuming no [REDACTED] is exercised, no additional Shares are [REDACTED] pursuant to the Pre-[REDACTED] Share Option Scheme and an [REDACTED] of HK\$[REDACTED] per [REDACTED] (being the mid-point of the [REDACTED] stated in this Document).

We intend to use the [REDACTED] from the [REDACTED] for the purposes set forth below:

- Approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for the development and commercialization of memory products. We will leverage our competitive strengths to solidify our market position in consumer-grade application scenarios while strategically expanding into diverse application scenarios. Specifically:
 - (i) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for the expansion our memory product R&D team and sales team. We plan to recruit approximately 260 R&D talents over the next three years, such as NAND analysis engineers, firmware engineers, application software engineers, application engineers and others, to meet the evolving product demands of end-users and maintaining our technological leadership. We also plan to recruit additional approximately 30 field application engineers skilled in memory product industry over the next three years to enhance our ability to respond to customer needs in a timely manner. The average annual salary for these personnels is expected to range from RMB400 thousand to RMB500 thousand.
 - (ii) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for purchasing equipments suitable for memory product development over the next three years, consisting of (a) R&D equipment such as high-speed digital oscilloscopes, protocol analyzers and logic analyzers; and (b) supporting professional software. We intend to procure, among others, 24 high-speed digital oscilloscopes at an estimated unit price of RMB3.1 million to RMB4.8 million, 31 protocol analyzers at an estimated unit price of RMB0.2 million to RMB2.8 million, and 30 logic analyzers at an estimated unit price of RMB2.3 million.
 - (iii) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED] million, is expected to be used for purchasing advanced testing equipment over the next three years to conduct additional in-house testing on certain high-end embedded storage and DRAM. We intend to procure, among others, 41 aging test devices at an estimated unit price of RMB1.6 million, 30 test sorting machines at an estimated unit price of RMB1.4 million, and 84 automated conveyor systems at an estimated unit price of RMB0.5 million to RMB1.0 million.
 - (iv) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for leasing or purchasing properties in Shenzhen and Hefei, Anhui Province that support the development and commercialization of memory products, including office areas for R&D personnel, laboratories and testing facilities. Specifically, over the next three years, we plan to lease or purchase additional properties with an aggregate gross floor area of approximately 7,000 sq.m. in Shenzhen and Hefei, Anhui Province for office areas for R&D personnel and laboratories. In

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addition, over the next three years, we plan to lease or purchase additional properties in Pingshan, Shenzhen, including approximately 6,450 sq.m. for memory product testing facilities and approximately 400 sq.m. for employee dormitories.

See “Business — Our Strategies — Continuously develop memory products, expand diverse application scenarios.”

- Approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for development and iteration of the controller chips. Controller chips are a core component of our end-to-end technology platform and are critical to the performance, power efficiency and reliability of our memory products. This investment will support the parallel development and upgrade of multiple controller chip models across major interfaces, covering key activities such as design, tape-out, validation and optimization. We will focus on the following areas to advance the design and development of advanced controller chips, including interface upgrades, process optimization and density breakthroughs. Specifically:
 - (i) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for covering the wafer tape-out expenses in our controller chip R&D projects over the next three years, consisting of MPW tape-out expenses and full-mask wafer tape-out expenses.
 - (ii) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for acquiring equipment suitable for controller chip R&D over the next three years, consisting of R&D equipment and supporting professional software. We intend to procure the following R&D equipment: 400 field-programmable gate array prototyping platforms at an estimated unit price of RMB0.05 million to RMB0.2 million, four high-speed digital oscilloscopes at an estimated unit price of RMB3.1 million to RMB4.8 million, and six protocol analyzers at an estimated unit price of RMB2.8 million, among others.
 - (iii) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for expanding our controller chip R&D team. We intend to recruit approximately 175 R&D talents over the next three years, including chip architecture engineers, integrated circuit engineers, design verification engineers, firmware development engineers, firmware verification and validation engineers, integrated circuit verification engineers and application engineers. The average annual salary for these personnels is expected to ranges from RMB450 thousand to RMB550 thousand.
 - (iv) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for leasing or purchasing properties in Shenzhen and Hefei, Anhui Province that support the R&D of controller chips, including office areas for R&D personnel, laboratories and testing facilities. Specifically, over the next three years, we plan to lease additional properties with an aggregate gross floor area of approximately 15,000 sq.m. in Shenzhen and Hefei, Anhui Province for office areas for R&D personnel, and approximately 2,600 sq.m. for laboratories.

See “Business — Our Strategies — Continue investing in technology capabilities and product development.”

- Approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED] million, is expected to be used for enhancing our in-house wafer-level inspection and controller chip validation, verification and quality-management testing capabilities. Rigorous validation, reliability, compatibility and quality-control testing across different wafers, process conditions and application scenarios is critical to ensuring the performance and quality of our controller chips and memory products.

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This investment will strengthen our in-house testing systems and infrastructure to support faster product qualification and iteration and more reliable mass production with our supply chain partners. We will establish an advanced validation and testing center that covers wafer-level inspection and validation, BGA validation testing and reliability testing. Specifically,

- (i) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for acquiring a range of advanced testing equipment and supporting auxiliary facilities over the next three years. We intend to procure, among others, three chip core testers at an estimated unit price of RMB13.0 million, three probe stations at an estimated unit price of RMB10.2 million, and three automatic test equipment at an estimated unit price of RMB16.5 million, among others.
- (ii) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for expanding our testing team. We intend to recruit approximately 90 testing team members, including software architecture engineers, software development engineers, firmware development engineers and software validation engineers for our testing center, over the next three years.
- (iii) approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for leasing or purchasing properties in Shenzhen for the testing center, including wafer testing area and controller chip testing area. Specifically, such properties are expected to include approximately 15,000 sq.m. for wafer testing area, approximately 5,250 sq.m. for controller chip testing area and approximately 1,000 sq.m. for employee dormitories.

See “Business — Our Strategies — Continue investing in technology capabilities and product development.”

- Approximately [REDACTED]% of the net [REDACTED], or HK\$[REDACTED], is expected to be used for working capital and for general corporate purposes.

The above allocation of the [REDACTED] will be adjusted on a pro rata basis in the event that the [REDACTED] is fixed at a higher or lower level compared to the mid-point of the estimated [REDACTED]. If the [REDACTED] is determined at HK\$[REDACTED] per [REDACTED], being the low end of the [REDACTED] stated in this document, after deducting the [REDACTED] fees and [REDACTED] and estimated expenses payable by us in connection with the [REDACTED], we will receive net [REDACTED] of approximately HK\$[REDACTED]. If the [REDACTED] is determined at HK\$[REDACTED] per [REDACTED], being the high end of the [REDACTED] stated in this document, after deducting the [REDACTED] fees and [REDACTED] and estimated expenses payable by us in connection with the [REDACTED], we will receive net [REDACTED] of approximately HK\$[REDACTED]. To the extent that the net [REDACTED] of the [REDACTED] are not immediately used for the above purposes or if we are unable to put into effect any part of our future development plans as intended, we will only deposit such net [REDACTED] into short-term interest-bearing accounts at licensed commercial banks and/or other authorized financial institutions (as defined under the Securities and Future Ordinance or the applicable laws and regulations in other jurisdictions). We will make an appropriate announcement if there is any change to the above proposed use of [REDACTED].